



ST. VINCENT PALLOTTI
COLLEGE OF ENGINEERING & TECHNOLOGY, NAGPUR

Faculty Development Program
On
RECENT TRENDS IN VLSI TECHNOLOGY
4TH-8TH DECEMBER, 2017

Organized By:
Department of Electronics &
Telecommunication Engineering

In association with
Electronics & ICT Academy
Indian Institute of Information
Technology, Design & Manufacturing
Jabalpur

Registration Form

Name of the Applicant :- _____

Qualification:- _____

Designation:- _____

Department:- _____

College / Industry : _____

Contact No:- _____

Email ID:- _____

Signature of Applicant:-

TARGET AUDIENCE

Faculty from Engineering Colleges
(ETRX, E&C, E & TC, EE, CSE, CE and allied fields)

Registration Fees: Rs. 1500/-

Accommodation will be provided for
outstation candidates on payment basis.

How to Apply

Registration for the FDP can be confirmed online,
telephonically or by email using the registration link
given below,
<https://goo.gl/S4Hj1q>

Payment can be done through NEFT,
bank details are given below:

Account Name: St. Vincent Pallotti
College of Engineering & Technology
Account No.: 13390100069244
Name of the Bank: The Federal Bank Limited,
Ramdaspath, Nagpur
IFSC Code : FDRL0001339

Last date of Registration
2nd December 2017

Contact Details
Prof. M. Shriraghavan
Mobile: 9011081252
email : smadbushi@stvincentngp.edu.in



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Gavsi Manapur, Wardha Road, Nagpur - 441108
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ST. VINCENT PALLOTTI
COLLEGE OF ENGINEERING & TECHNOLOGY, NAGPUR

One week
Faculty
Development
Program
on

**RECENT
TRENDS IN
VLSI
TECHNOLOGY**
4th-8th
December
2017

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**Department of Electronics & Telecommunication
Engineering**

In Association with



Electronics & ICT Academy
Indian Institute of Information Technology,
Design & Manufacturing, Jabalpur

Vision of Department of E&TC

To develop disciplined & ethically strong Electronics &
Telecommunication engineers having multi-disciplinary
knowledge with entrepreneurship & leadership skills to
serve the society.

Mission of Department of E&TC

To hone young minds with knowledge in the field of
Electronics & Telecommunication Engineering & related
areas with a focus on developing professional
competencies through proactive industry collaborations,
encouraging students for higher studies & research
activities, inculcating in them a spirit of service &
a concern for the environment.

◀ THE INSTITUTE & THE DEPT. OF E & TC

St. Vincent Pallotti College of Engineering & Technology established in 2004, is one of the premier institutes in central India. The college, managed by the Pallottine Fathers, aims to provide quality technical education and adheres to discipline and personal integration, to make its students fit for the upcoming technological challenges. The college offers undergraduate programs in Mechanical Engg., Electrical Engg., Electronics & Telecommunication Engg., Computer Engineering, Information Technology and two Post Graduate programs in CAD & CAM and CSE. It is a center for higher learning & research (Ph. D. research center) approved by RTM, Nagpur university.

The Department of Electronics and Telecommunication Engineering has an intake of 120 students, 29 experienced and dedicated faculty members with good industry connect. It has well equipped laboratories and facilities like the e-yantra lab. The department is very well known for the excellent academic performance of the students and is also in the forefront of conducting technical and soft skills development programs for students and faculty members.

THE E & ICT ACADEMY, IIITDM, JABALPUR

The Department of Electronics and Information Technology, Government of India has instituted seven Electronics and ICT Academies with one academy at Indian Institute of Information Technology, Design and Manufacturing (IIITDM), Jabalpur. The Academy aims at the design and implementation of scalable training programmes in niche areas of electronics and ICT for the development of required knowledge base, skills and tools to unleash the talent of Indian population.

◀ ABOUT THE FACULTY DEVELOPMENT PROGRAM (FDP)

This FDP on VLSI is being organized in collaboration with Indian Institute of Information Technology, Design and Manufacturing (IIITDM), Jabalpur. It aims at enhancing the skills of faculty members, post graduate students and industry persons focusing on the latest trends in VLSI technology with a rich blend of theory and practical sessions and will be beneficial for all participants from Electronics, Telecommunication, and allied disciplines.

TOPICS OF FDP

- Synthesis for Low Power: A New VLSI Design Paradigm
- Design and Analysis of PLL for SoC
- Nanoscale Semiconductor Devices for Low Power VLSI Design
- Power Semiconductor Devices
- MEMS and Interconnects
- Verification
- Back end tools
- Laboratory sessions on VLSI by industry and academic experts

LEARNING OUTCOMES

Participants will:-

- get acquainted with latest trends and technology in VLSI
- get hands on experience with various tools
- be able to offer good academic projects for both UG and PG students
- explore topics to pursue their research in VLSI domain

RESOURCE PERSONS

Dr. Ajit Pal,
Professor, Dept. of CSE, IIT Kharagpur

Dr. Saurabh Saxena,
Assistant Professor, Dept. of EE, IIT Madras

Dr. Balwinder Raj,
Assistant Professor, Dept. of ECE, NIT Jalandhar

Prof. P. N. Kondekar,
Professor, Dept. of ECE, IIITDM Jabalpur

Dr. R. Patrikar,
Professor, Dept. of ECE, VNIT Nagpur

Dr. S.S. Limaye,
Professor, Dept. of E&TC, SVP CET Nagpur

Dr. Meghana Hasamnis,
Professor, Dept. of Electronics, RCOEM, Nagpur

Mr. Jitendra Zalke,
Asst. Professor, Dept. of EDT, RCOEM, Nagpur

Industry Experts from
Trident Tech. Labs Pvt. Ltd., Pune

◀ CHIEF PATRONS

Fr. Varghese Pullan
Chairman, Nagpur Pallottine Society

Fr. George Madeikkal
Director, SVP CET, Nagpur

Prof. Pramod Kumar Jain
Director, IIITDM, Jabalpur

◀ ADVISOR

Dr. V. V. Sohoni
Principal, SVP CET, Nagpur

◀ CO-ORDINATORS

Dr. S. S. Limaye
Professor, Dept. of E&TC, SVP CET, Nagpur

Prof. P. N. Kondekar
Professor, Dept. of ECE, IIITDM, Jabalpur

Prof. M. N. Kapse
Head, Dept. of E&TC, SVP CET, Nagpur